IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s) Brian J. Corcoran et al.	Group Art Unit: 2166
Serial No.: 10/718,420	Examiner: Srirama T Channavajjala
Filed: November 20, 2003	
Title: APPARATUS, SYSTEM AND METHOD FOR COMMUNICATING A BINARY CODE IMAGE	
Attorney Docket No.: TUC920030130US1	

Commissioner for Patents
Washington, DC. 20231

INFORMATION DISCLOSURE STATEMENT

Dear Sir:

This Information Disclosure Statement is submitted:

_____ under 37 CFR 1.97(b), or
(Within three months of filing national application; or date of entry of international application; or before mailing date of first office action on the merits; whichever occurs last)

_____ under 37 CFR 1 .97(c) together with either a:
_____ Statement under 37 CFR 1.97(e), or
_____ a $180.00 fee under 37 CFR 1.17(p), or
(After the CFR 1.97(b) time period, but before final action or notice of allowance, whichever occurs first)

_____ under 37 CFR 1.97(d) together with a:
_____ Statement under 37 CFR 1.97(e), and
_____ a $180.00 fee set forth in 37 CFR 1 .17 (p).
(Filed after final action or notice of allowance, whichever occurs first, but before payment of the issue fee)

   X   under 37 CFR 1.97(e),

           each item of information contained herein was first cited in a communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of the information disclosure statement.

   X no item of information contained herein was cited in a communication from a foreign patent office in a counterpart foreign application nor was known to any individual designated in §1.56(c) more than three months prior to the filing of the information disclosure statement.

   X   Applicant(s) submit herewith Form PTO/SB/08a-Information Disclosure Statement together with copies, of patents, publications or other information of which applicant(s) are aware, which applicant(s) believe(s) may be material to the examination of this application and for which there may be a duty to disclose in accordance with 37 CFR 1.56.

The relevance of the attached references is that this is the closest art of which Applicant is aware. Applicant submits that the above references taken alone or in combination neither anticipate nor render obvious the present invention. Consideration of the foregoing in relation to this application is respectfully requested.

It is requested that the information disclosed herein be made of record in this application.

Design of Booting System Based on Flash

Abstract:
For a booting system based on Flash in a signal processing system, it is vital whether Flash works in order or not. In this paper, based on the DSP speech communication system developed by us, the structure, we introduce the system host processor --- High Speed Fixed-Point Digital Signal Processor TMS320C5410 and Flash Memory M29W400T storing chip. Our focuses are on the principle of memory mapping, the structure of system booting based on Flash and problems in designing experiments. We also give some solution to the problems and some experiment results.

Respectfully submitted,

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